



PTO/SB/08B (Modified)

Substitute for form 1449B/PTO  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (use as many sheets as necessary)				<b>Complete If Known</b>	
				Application Number	10/790,205
				Filing Date	March 2, 2004
				First Named Inventor	Shen et al.
				Group Art Unit	<del>Not Assigned</del> 2193
				Examiner Name	<del>Not Assigned</del> Chat Do
Sheet	1	of	1	Attorney Docket Number	95-528

OTHER PRIOR ART — NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
CD		STEVEN W. SMITH, Ph.D., "The Fast Fourier Transform", <i>The Scientists and Engineer's Guide to Digital Signal Processing</i> , Chapter 12, from <a href="http://www.dspguide.com">http://www.dspguide.com</a>	
CD		Li et al, "Efficient Radix-4 and Radix-8 Butterfly Elements", NorChip99, Oslo, Norway, November 1999, <a href="http://www.es.isy.liu.se/publications/papers_and_reports/1999/weidongli_NorChip99.pdf">http://www.es.isy.liu.se/publications/papers_and_reports/1999/weidongli_NorChip99.pdf</a>	
CD		IEEE Std. 802.11a-1999, "Part 11: Wireless LAN Medium Access Control (MAC) and Physical Layer (PHY) specifications: High-speed Physical Layer in the 5 GHz Band", LAN/MAN Standards Committee of the IEEE Computer Society, IEEE-SA Standards Board, Approved September 16, 1999, New York, USA	

Examiner Signature	/Chat Do/ (03/01/2007)	Date Considered	03/01/2007
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>1</sup> Unique citation designation number. <sup>2</sup> Applicant is to place a check mark here if English language Translation is attached.



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		Group Art Unit	<del>2124</del> 2193		
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CD		SON <i>et al.</i> , "A High-Speed FFT Processor For OFDM Systems", PROCEEDINGS OF THE 2002 INTERNATIONAL SYMPOSIUM ON CIRCUITS AND SYSTEMS (ISCAS 2002), May 26-29, 2002, pp. III-81-284, IEEE, USA	
CD		LO <i>et al.</i> , "Design of an Efficient FFT Processor for DAB System", PROCEEDINGS OF THE 2001 INTERNATIONAL SYMPOSIUM ON CIRCUITS AND SYSTEMS (ISCAS 2001,) May 6-9, 2001, pp. IV-654-657. IEEE, USA	
CD		JOHNSON, "Conflict Free Memory Addressing for Dedicated FFT Hardware", IEEE TRANSACTIONS ON CIRCUITS AND SYSTEMS II: ANALOG AND DIGITAL SIGNAL PROCESSING, Vol. 39, No. 5, May 1, 1992, pp. 312-316, IEEE, USA	
CD		MA <i>et al.</i> , "A Hardware Efficient Control of Memory Addressing for High-Performance FFT Processors", IEEE TRANSACTIONS ON SIGNAL PROCESSING, Vol. 48, No. 3, March 3, 2000, pp. 917-921, IEEE, USA	

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